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Je-Chang et al.

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(54) **SIGNAL COMPRESSING SYSTEM**

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(57) **ABSTRACT**

A multi-scanner circuit includes a circuit for scanning a signal using several different patterns and scanning pattern selector circuit for determining which of the scanning patterns produces the most efficient coding result. The selector circuit then outputs a coded signal, which signal represents the most efficiently coded signal, and a selection identification signal, which identifies the scanning pattern found to be most efficient. In an exemplary case wherein runlength coding is to be used, the selector selects the most efficient scanning pattern for runlength coding.

15 Claims, 3 Drawing Sheets

